Monday, September 10

10.30 hs - 12.10 hs : Tutorial 1
Adrian Ionescu : "RF MEMS : fabrication technologies and packaging"

14.00 hs - 15.40 hs : Tutorial 2
Y. Danto, F. Verdier : "New approach of reliability assessment in electronics"

16.00 hs - 17.40 hs : Tutorial 3
Tuesday, September 11

10.30 hs - 12.10 hs: Tutorial 4:
Adrian Ionescu: "Electrical characterization of thin semiconductor films by the pseudo MOS method"

14.00 hs - 15.40 hs: Tutorial 5:
Y. Danto, F. Verdier: "New approach of reliability assessment in electronics"

16.00 hs - 17.40 hs: Invited paper:
Adrian Ionescu: "MEMS for Reconfigurable Wide-Band RF IC’s"
Wednesday, September 12

8.50 hs - 10.10 hs : Session 1 : SOI
M.A. Pavanello, J.A. Martino, D. Flandre, "Improved current mirror output performance by using graded-channel SOI NMOSFETs"
N.A.E. Forhan, N. Braga, J.L. Gonçalves, "Analysis of process parameters in "Smart-Cut" SOI Structure Fabrication"
A.S. Nicolett, J.A. Martino, E. Simoen, C. Claey, "A New Technique to Extract the Oxide Charge Density at Front and Back Interfaces of SOI nMOSFETs Devices"
V. Sonnenberg, J.A. Martino, "New Methods for Determining the Silicon Film Doping Concentration and the Back Interface Oxide Charge Density Using SOI-MOS Capacitor"

8.50 hs - 10.10 hs : Session 2 CAD & Test
C.E.T. Oliveira, A.P.V. Pais, M.L. Anido, "A Distributed Object-Oriented Design Rule Checker"
J.D. Togni, A.I. Reis, R.P. Ribas, "WEB-Based Automatic Layout Generation Tool with Visualization Features"

10.30 hs - 12.30 hs  Session 3 : Process Steps & Reliability
Y. Danto, J-Y Deletage, F. Verdier, H. Fremont, "Evolution of reliability assessment in PCB assemblies" (Invited)
G. Gautier, F. Raoult, O. Bonnaud, C. Viana, N. Morimoto, "Development of a Low Temperature N and P Type TFT's Fabrication Process by LPCVD and SPC Techniques"
A.N.R. da Silva, N.I. Morimoto, O. Bonnaud, "The Influence of process parameters on the electrical behavior of silicon oxide thin films deposited by PECVD-TEOS"
C. Reyes-Betanzo, S. Moshkalyov, M.A. Pavanello, A.C. Ramos, J.W. Swart, "Plasma Etching of Si3N4 with High Selectivity Over Si and SiO2"
R. Ruas, R. Mansano, P. Verdonck, N. Braithwaite, "The influence of electrode material on argon plasmas"

10.30 hs - 12.30 hs  Session 4 : Analog Design
A. Olmos, A. Gomes, R. Takiguti, F. Clayton, "A bandgap adjustment strategy for temperature centering and spread reduction"
S.A. Haddad, W.A. Serdijn, "Low-Power Adaptive Bipolar Low Noise Amplifier"
C.E. Paghi, S. Gùthes, "Circuit topologies for Peltier effect anemometer"
C.A. dos Reis Filho, "A miniature in-head transmitter with linear response for resistance temperature detectors"
J.P. Oliveira, N. Oki, "An Analog Implementation of Radial Basis Neural Networks (RBNN) using BiCMOS Technology"
W. Prodanov, M.C. Schneider, "A digitally programmable current schmitt-trigger" (P145_JTK2QG.PDF)
Thursday, September 13

8.50 hs - 10.10 hs : Session 5 : Devices & Parameter Extraction
R.W. Reis, S.G. dos Santos Filho, "Characterization of Ni/TiSi2 contacts fabricated onto n+p junctions"
H.E.M. Peres, E. Galeazzo, M.O.S. Dantas, F.J.R. Fernandez, "Porous Silicon Masking by Silicon Oxide and Hydrogen Ion Implantation"
A. Cerdeira, M. Estrada, R. Garcia, A. Ortiz-Conde, F.J. García, "New procedure for the extraction of AIMSpice level 15 a-Si:H TFT model parameters"

10.30 hs - 12.30 hs : MEMs & Sensors
Jorge J. Santiago-Aviles, Barry Taylor, Patricio Espinoza-Vallejos, Mario Gongora-Rubio, Luis Sola-Laguna, "Photo Imageable LTCC Tapes for Electronic Packaging and 3-D Meso-scale Structures" (Invited)
E.W. Simões, R. Furlan, M.T. Pereira, "Design of microfluidic oscillators"
H.P. Araújo, S.G. dos Santos Filho "Simulations of an Interference Birefringent Thin-Film Filter Used as a Narrow-Band Polarizer"

10.30 hs - 12.30 hs : Digital Design
J. Guimaraes, "Circuit analysis of a single-electron 2-island SET subcircuit"
W. Encinas, C. Duenas, "Functional Verification in 8-bit Microcontrollers: A Case Study"
M. Adamski : "Formal Methods in Specification and Synthesis of Petri net based Reprogrammable Logic Controllers" (invited)

14.00 hs - 16.30 hs : Poster session
Leonardo Mesquita, Galdenoro Botura, Osamu Saotome, "A Translinear Membership Function Circuit Design in CMOS Technology"
Marcello Bellodi, Benjamin Iniguez, Denis Flandre, João A. Martino, "A simple leakage drain current model for accumulation-mode SOI pMOSFETs operating up to 300oC"
Salvador Pinillos Gimenez, Marcelo Antonio Pavanello, João Antonio Martino, "Behavior of the graded-channel fully-depleted SOI NMOSFET in subthreshold regime"
Alessandro R. Oliveira, Inés Pereyra, Marcelo N. Carreño, "Monte Carlo Simulation of Doping by Ionic Implantation into a-SiC:H"
Thursday, September 13 - continuation

14.00 hs - 16.30 hs : Poster session - continuation

Marcelo A. Hayashi, José E. C. de Queiroz, José A. Diniz, Jacobus W. Swart, "Shallow Junction Formation with Preamorphization and Rapid Thermal Annealing of Boron Implanted Silicon"

Luís da Silva Zambom, Ronaldo Domingues Mansano, "Silicon nitride deposited by inductively coupled plasma using dichlorosilane and ammonia"

Nasser M. Hasan, Sebastião G. dos Santos Filho, Walther Schwarzecher, "Temporal Evolution of Roughness in Electroless Copper Films"

Stanislav Moshkalyov, Claudia Reyes-Betanzo, Ricardo C. Teixeira, Ioshiaki Doi, Maria B. P. Zakia, Jacobus W. Swart, José A. Diniz, "Etching of Polycrystalline Silicon in SF6 Containing Plasmas"


Ricardo C. Teixeira, Ioshiaki Doi, Maria B. P. Zakia, Jacobus W. Swart, José A. Diniz, "Grain size influence on sheet resistance of P- and As-implanted polycrystalline silicon deposited by vertical CVD reactor"

Leandro Z. Toquetti, William A. Nogueira, Sebastião G. Santos Filho, "A Practical Procedure to Match the Measured Capacitance of Low and High Frequency in Order to Obtain the Energy Distribution of the Interface States Density"

Carlos A. S. Ramos, Manuel Cid Sánchez, "Contactless Determination of Minority-Carrier Lifetimes by Photoconductance Measurements"

Tomas D. Becerril, Hector J. Santiesteban, Peter P. Hryrstov, Marcelo G. Cuamatzi, Javier O. Hernandez, "Field enhanced generation in MOS structures containing defects"

Tomas D. Becerril, Hector J. Santiesteban, Araceli V. Vicuña, Marcelo G. Cuamatzi, Javier O. Hernandez, "Light sensor device using a MIS structure"

Acacio Siarkowski, Douglas Bulla, Nilton Morimoto, "Integrated Mach-Zehnder Interferometer Simulation Results for Optical Pressure Sensor"

Hamilton F. Moraes, Antonio C. Seabra, "Produccion and characterization of a thick film of PMMA (poly methylmethacrylate) for application in integrated microoptics"

Antonio Arruda, Ronaldo Mansano, Ana Paula Mousinho, Ronaldo Ruas, Lilian Hanamoto, Maria Felisberti, "Optimization of Thermal Process Employed in Polymer Micromachines and Microlithography"

Janina Falco, Flavio Aristone, Luiz Otavio Ferreira, "Nickel electroforming of LIGA processed micro-structures"

Marcelo Bariatto A. Fontes, Rogério Furlan, Idágene Aparecida Cestari, "Microelectrode Array for Cardiac Potential Mapping"

Roberto J Rodrigues, Rogério Furlan, "Microsensor for Liquid Flow Measurements"


Hugo R. J. Grados, Marcelo A. Pavanello, Jacobus W. Swart, Ioshiaki Doi, José A. Diniz, "Project and Development of an educational CMOS process"

Leandro T. Manera, José A. Diniz, Peter J. Tatsch, Jacobus W. Swart, "An E/D nMOS Technology for Educational and Multi-Project Chip Activities"
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